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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Eric R. Fossum

Examiner: Marcos D Pizarro Crespo

Patent No.: 7,365,409

Group Art Unit: 2814

Issue Date: April 29, 2008

Docket No: 2002-0111.02/US

Title: TWO-TRANSISTOR PIXEL WITH BURIED RESET CHANNEL AND METHOD OF FORMATION

Commissioner for Patents
Office of Patent Publication
ATTN: Certificate of Correction Branch

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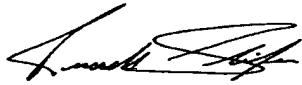
- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
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Customer Number: 26809

Respectfully submitted,

Date: May 13, 2008

By: 

Russ Slifer
Reg. No. 39,838

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Christine Hartness

Name_____
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Patentee: Eric R. Fossum

Title: TWO-TRANSISTOR PIXEL WITH BURIED RESET CHANNEL AND METHOD OF
FORMATION



REQUEST FOR CERTIFICATION OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

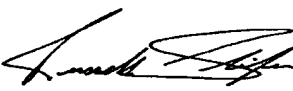
As the error is that of the Patent Office, it is believed that no fee is due.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,365,409

Page (1) of 1

DATED : April 29, 2008

INVENTOR(S) : Fossum

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 8, line 55, in Claim 5, delete "Ff/ μm^2 ." and insert -- fF/ μm^2 . --, therefor.

In column 9, line 18, In Claim 9, delete "Ff/ μm^2 ." and insert -- fF/ μm^2 . --, therefor.

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